EE/CprE/SE 491 WEEKLY REPORT 09

11/08/24 – 11/14/24 Group number: sdmay25-31 Project title: i281 Client & Advisor: Prof. Alexander Stoytchev Team Members: Ethan Uhrich - Team Lead & Treasurer Ariana Dirksen - Editor & Note Taker Tessa Morgan - Task Manager & Webmaster Gigi Harrabi - Client Interaction & Outreach Coordinator

Weekly Summary

The focus of this week was reworking our PC implementations after a design mistake was found. Then we tested our new PC implementations and worked on fleshing out the documentation for the PC lab. We also finished designing the implementation for Lab 1. We also learned how to prepare and order PCB implementations.

Past Week Accomplishments

Ethan Uhrich: Implemented revised PC design

Ariana Dirksen: Finished Designing Lab 1, Begun Lab 1 implementation. Revised PC Design, Implemented revised PC design with cut wire.

Tessa Morgan: Re-worked PC layout and completed PC with kit wires. Started simulating the PC.

Gigi Harrabi: Submitted the revised version of the mux lab.

Individual Contributions

NAME	Individual Contributions (Quick list of contributions. This should be short.)	Hours this week	HOURS cumulative
Ethan Uhrich	Implemented revised PC design	4	44
Ariana Dirksen	Finished Designing Lab 1 and began implementation. Revised PC Design and implemented revised design.	7	52
Tessa Morgan	Re-worked PC layout and completed PC with kit wires. Started simulating the PC	8	47
Gigi Harrabi	Revised the mux lab and sent it to our advisor for another revision	5	42

Plans for the Upcoming Week

Ethan Uhrich: Help with implementation of Lab 1 and Rough Draft

Ariana Dirksen: Finish implementation and test for Lab 1, finish rough draft for Lab 1. Continue work on PC rough draft.

Tessa Morgan: Finish PC implementation, work on PC lab activity section. Look into EEPROM programmer.

Gigi Harrabi: help finish implementation document of lab 1 and pc lab.

Summary of Weekly Advisor Meeting Agenda

- Circuit Check Rewired PC, Tester, MUX x2
- Deciding Which Layout to Use for the PC Writeup
- Circuit Conventions
- PC Circuit Design Revisions